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Time: Wednesday, December 12, 2007, 08:45 - 09:30
Place: Auditorium
Chair: Franco Fummi; Univ Verona, Italy
Microsystems Engineering from Nano to Micro and Macro
Andreas G. Andreou, Johns Hopkins University

A2L-A Keynote: Giovanni DeMicheli
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Place: Auditorium
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New Design Paradigms: New Architectures for New Technologies
Giovanni Demicheli, Ecole Polytechnique Federale de Lausanne
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Ali Douik, Jalel Ghabi, Messaoud Hassani, Ecole Nationale d'Ingénieurs de Monastir

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Mateus Felzke Schonardie, Denizar Cruz Martins, Federal University of Santa Catarina and Power Electronics Institute
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Time:    Thursday, December 13, 2007, 08:30 - 09:15
Place:   Auditorium
Chair:   Abdelhakim Hafid; Univ de Montréal, Canada

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Ahmed Al-Gindy, Ayman Tawfik, Ajman University of Science & Technology; Hussain Al-Ahmad, Etisalat University College; Rami Qahwaji, University of Bradford

An Improved SDA Approach with DFB PREPROCESING for Face Recognition

Walid Boukabou, Ahmed Bouridane, Queens University, Belfast; Camel Tanougast, Nancy University - Université Henri Poincaré

Tracking Objects in Videos with Texture Features

John Klein, Christele Lecomte, Pierre Miche, LIITIS

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Abolfazl Lakdashti, Rouzbahan Institute of Higher Education; Hedayat Kialashaki, University of Applied Science and Technology; Mohammad-T Manzuri-Shalmani, Sharif University of Technology; Hossein Ajorloo, Iran Telecommunication Research Center
B2L-E  Microwave Circuits and Structures

Time:    Thursday, December 13, 2007, 09:20 - 10:50
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Time: Thursday, December 13, 2007, 10:50 - 11:15
Place: Poster Area 1
Chair: A. Kherras; EMI, Morocco

VHDL-AMS Modeling of an UWB Radio Link Including Antennas
Rami Khouri, Yvan Duroc, Vincent Berouille, Smail Tedjini, INP Grenoble - LCIS; Osman Franscico Oliveira Gomes, Campina Grande - PB

Efficient Reference Signal Cancellation in a Passive Radar
Pawel Biernacki, University of Technology Wroclaw

30GHz Amplifiers with 0.25um SiGe BiCMOS
Jorge Torres, Instituto Militar dos Pupilos do Exército; João Caldeias Vaz, João Costa-Freire, Instituto Superior Técnico

Theoretical and Simulation of Crosstalk Between Two Strip Lines Using Multiconductor Transmission Line
Said Ghnimi, Adnen Rajhi, Ali Gharsallah, Faculté des Sciences de Tunis

Reduction of Power Consumption in a Viterbi Decoder for OFDM-WLAN
Fabian Angarita Preciado, Maria Jose Canet Subiela, Trinidad Sansaloni Balaguere, Vicenç Almenar Terre, Javier Valles Coquillat, Universidad Politècnica de Valencia

SecurID Authenticator: on the Hardware Implementation Efficiency
Nicolas Sklavos, University of Patras; Costas Efstathiou, TEI of Athens

Efficient and Effective VLSI Architecture for a Wavelet-Based Broadband Sonar Signal Detection System
Sheng Cheng, Chien-Hsun Tseng, Marina Cole, University of Warwick

A Wideband CMOS LNA Integrated with Balun and Linear Digital Gain Control
Fernando Azevedo, Fernando Fortes, Instituto Superior de Engenharia de Lisboa; João Vaz, Instituto Superior Técnico / Instituto de Telecomunicações; M. João Rosario, Instituto Superior Técnico

A Real-Time Admission Control and Planning of OLS Networks
Yassine Khilifi, Noureddine Boudriga, University of Carthage; Mohammad Obaidat, Monmouth University

Design and Implementation of a 1-V Low Flicker Noise Direct Conversion Mixer at 5.5 GHz Using a 90nm CMOS RF Technology
Simitisakis Paschalis, Kitonaki Eleni-Sotiria, Bazigos Antonios, Papananos Yannis, National Technical University of Athens

Characterization Test Plan for a PCB to Die Transition in a fcCBGA Application
Joseph Romen Cubillo, Jean Gaubert, Sylvain Bourdel, Hervé Barthélémy, L2MP

Advanced Model and RF-CMOS Design of the Boot-Strapped Inductor
Domenico Zito, Alessandro Fonte, Bruno Neri, University of Pisa

Performance Analysis of Single Code Spread ALOHA Systems
Ali Hellany, Hassan Achi, University of Western Sydney

Design of the Microstrip Antennas Lambda/8 for Wireless Communications “by TLM Model”
Latif Adnane, Rachid Hilal, Abdellah Ait Ouahman, University Cadi Ayyad
B4L-A  SPECIAL SESSION: Design of Wireless Sensor Networks - The ANGEL Project Approach

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Mehdi Kamal; Sharif Univ of Technology, Iran

Timing-Driven Steiner Tree Construction with Wire Sizing, Buffer Insertion and Obstacle Avoidance
Jin-Tai Yan, Shi-Qin Huang, Zhi-Wei Chen, Chung-Hua University

Hardware Architectures for the Generalized Finite Automata Algorithm
Abdel Ejnioui, University of South Florida Lakeland; Paul Bao, University of South Florida Lakeland,
Information Technology Dept.

Order Independent Switching Operations in Radially Operated Networks
Pedro Carvalho, Alena Kostalova, Luis Ferreira, Instituto Superior Tecnico, UTL

No-Handshake Asynchronous Survivor Memory Unit for a Viterbi Decoder
Wei Shao, Linda Brackenbury, University of Manchester

Dual Mode K-Best MIMO Detector Architecture and VLSI Implementation
Ramin Shariat-Yazdi, Tadeusz Kwasniewski, Carleton University
B5L-A  Keynote: Didier Lattard
Time:  Thursday, December 13, 2007, 02:00 - 02:45
Place:  Auditorium
Chair:  Eric Kerhervé; Bordeaux Univ, France

Trends in Complex SoC Design: From Technology Variability to Multiprocessor Architectures
Didier Lattard, CEA

B6L-A  Keynote: Kiyoo Itoh
Time:  Thursday, December 13, 2007, 02:45 - 03:30
Place:  Auditorium
Chair:  Yvon Savaria; École Polytechnique, Canada

Low-Voltage Limitations and Challenges of Memory-Rich Nano-Scale CMOS LSIs
Kiyoo Itoh, Hitachi; Riichiro Takemura, Hitachi CRL
SPECIAL SESSION: Device Modeling and Simulation

Time: Thursday, December 13, 2007, 03:35 - 05:05
Place: Auditorium
Chair: Andrei Vladimirescu; University of California, Berkeley, USA

Optimal Design Methodology for High-Order Continuous-Time Wideband Delta-Sigma Converters
Yi Ke, Soheil Radiom, Hamidreza Rezaee, Guy Vandenbosch, Katholieke Universiteit Leuven; Jan Craninckx, IMEC; Georges Gielen, Katholieke Universiteit Leuven

System-Level Design for Nano-Electronics
David Atienza, Ecole Polytechnique Federale de Lausanne; Shashi Kanth Bobba, LSI/EPFL; Massimo Poli, University of Siena; Giovanni De Micheli, LSI/EPFL; Luca Benini, DEIS/Bologna University

Sizing Low-Voltage CMOS Analog Circuits
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Ruimin Huang, Markus Becker, University of Freiburg - IMTEK; Niklas Lotze, University of Freiburg; Yiannos Manoli, University of Freiburg - IMTEK

High-Speed, Low Cost Parallel Memory-Based FFT Processors for OFDM Applications
Chin-Long Wey, Shin-Yo Lin, Wei-Chien Tang, Muh-Tien Shiue, National Central University

A Low-Complexity High-Performance Modulation Code for Holographic Data Storage
Chi-Yun Chen, Tzi-Dar Chiueh, National Taiwan University

A QoS-Oriented Scheme for a Real Time Admission Control in OBS Networks
Amor Lazzez, Noureddine Boudriga, University of Carthage

Roundoff Noise Analysis for FSK Signals in White Noise Represented as Signed Power-of-Two Numbers
Ya Jun Yu, Yong Ching Lim, Nanyang Technological University
B7L-D  Digital Filter Design II

Time:    Thursday, December 13, 2007, 03:35 - 05:05
Place:   Room 3
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       Recherche en Electronique et Electrotechnique de Nantes Atlantique
Circuit Level Optimizations

Time: Thursday, December 13, 2007, 03:35 - 05:05
Place: Room 5
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Seyed Ebrahim Esmaeili, Nabil Khachab, Kuwait University

Architectural and Control Contributions for PV Grid-Connected Systems Applying Dual-Stage Inverters
Marcio Mendes Casaro, Federal University of Technology - Paraná; Denizar Cruz Martins, Federal University of Santa Catarina

A Comparative Implementation of PCA Face Recognition Algorithm
Nicolas Morizet, Frédéric Amiel, Insaf Dris Hamed, Thomas Ea, Institut Supérieur d’Electronique de Paris

Algebraic PWM Strategy of a Multilevel NPC Voltage Source Inverter
D Boufassa, Farid Bouchafia, University of Science and Technology Houari Boumediene; El Madjid Berkou, École Nationale Polytechnique; M.S. Boucherit, Polytechnic National School Algiers

High-Gain Observer Compensator for Rotor Resistance Variation on Induction Motor RFOC
Ahmed Abbou, Ecole Mohammadia d’Ingénieurs; Hassan Mahmoudi, Ecole Mohammadia d’Ingénieurs; Abdelhadi Elbacha, Faculté des Sciences et Techniques, Marrakech

Improving Power Efficiency and Reliability in RF Tire Pressure Monitoring Modules
Fabrizio Iacopetti, Sergio Saponara, Luca Fanucci, University of Pisa

CAD Methodology for Analog Static CMOS Design Automation
François Rudloff, Edith Kussener, L2MP - ISEN, Toulon; Gaëtan Bracmard, ATMEL Rousset

Reducing Power Consumption in NoC Design with No Effect on Performance and Reliability
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A New ANFIS Based Learning Algorithm for CMOS Neuro-Fuzzy Controllers
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The Effect of Stator Resistance Variation on DTFC of Induction Motor and its Compensation
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Damian Grzechca, Tomasz Golonek, Jerzy Rutkowski, Silesian University of Technology

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Yifan Wu, Behrouz Nowrouzian, University of Alberta

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Ismail Assayed, Verimag; Sergio Yovine, CNRS/VERIMAG

FPGA-Based Low-Cost System for Automatic Tests on Digital Circuits
Luca Mostardini, Luca Bacchirelli, Luca Fanucci, University of Pisa; Lorenzo Bertini, Marco Tonarelli, Adolfo Giambastiani, Marco De Marinis, SensorDynamics AG
B9L-A  SPECIAL SESSION: Low Power Design Techniques

Time:    Thursday, December 13, 2007, 05:30 - 07:00
Place:   Auditorium
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Time:    Thursday, December 13, 2007, 05:30 - 07:00
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