# Table of Contents


<table>
<thead>
<tr>
<th>Section</th>
<th>Title</th>
<th>Authors</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>Preface</td>
<td></td>
<td>x</td>
<td></td>
</tr>
<tr>
<td>Keynote Addresses</td>
<td></td>
<td></td>
<td>xi</td>
</tr>
<tr>
<td>Conference Organizers</td>
<td></td>
<td></td>
<td>xiv</td>
</tr>
</tbody>
</table>

### Session 1: Evolvable Hardware

Analysis of Reconfiguration Options for a Reconfigurable Polymorphic Circuit ........................................... 3  
Zdenek Vasiček, Ladislav Capka, and Lukas Sekanina

Gate-Level Evolutionary Development Using Cellular Automata ........................................................................... 11  
Michal Bidlo and Zdeněk Vasiček

Intrinsic Evolution of Large Digital Circuits Using a Modular Approach ............................................................... 19  
Shri Vidhya Alagesan, Sruthi Kannan, G. Shanthi, A.P. Shanthi, and Ranjani Parthasarathi

Fitness Evaluation Expansion to Enhance GA’S Performance in Evolvable Hardware........................................... 27  
Elhadj Benkhelifa, Anthony Pipe, Mokhtar Nibouche, and Gabriel Dragffy

Comparing Evolvable Hardware to Conventional Classifiers for Electromyographic Prosthetic Hand Control......32  
Kyrre Glette, Thiemo Gruber, Paul Kaufmann, Jim Torresen, Bernhard Sick, and Marco Platzner

### Session 2: Adaptive Systems for Space Applications

On Convergence of Development Costs and Cost Models for Complex Spaceflight Instrument Electronics........43  
Semion Kizhner, Umeshkumar D. Patel, Robert L. Kasa, Phyllis Hestnes, Tammy Brown, and Meg Vootukuru

SoCWire: A Network-on-Chip Approach for Reconfigurable System-on-Chip Designs in Space Applications......51  
Björn Osterloh, Harald Michalik, Björn Fiethe, and Karel Kotarowski

Self-Organizing and Scalable Shape Formation for a Swarm of Pico Satellites....................................................... 57  
Carlo Pinciroli, Mauro Birattari, Elio Tuci, Marco Dorigo, Marco del Rey Zapatero, Tamas Vinko, and Dario Izzo

Adaptive Interference Rejection Filtering in On-Board Direct-Sequence / Spread-Spectrum Transponder for TT&C Applications: Analysis, Simulation & Breadboard Test Results........................................ 62  
L. Simone and F. Caselli

Flexible S-band SSPA for Globalstar 2 .................................................................................................................... 70  
A. Darbandi, M. Zoyo, J.Y. Touchais, and Y. Butel

SpaceWire for Adaptive Systems ............................................................................................................................. 77  
Steve Parke

Self-Reconfigurable Analog Array Integrated Circuit Architecture for Space Applications................................. 83  
Didier Keymeulen, Adrian Stoica, Ricardo Zebulum, Srinivas Katkoori, Pradeep Fernando, Harirahan Sankaran, Mohammad Mojarradi, and Taher Daud
Session 3: Built-in Self-Test and Self-Repair

Non-FPGA-based Field-programmable Self-repairable (FPSR) Microarchitecture......................................................... 93
Yong-Kyu Jung

An Approach for Recovering Satellites and their Cryptographic Capabilities in the Presence of SEUs and Attacks........................................................................................................................... 101
Marcio Juliato and Catherine Gebotys

Parameter Modelling Approach with Potential Applications for Self-Test of Biosensors ........................................... 109
H.J. Kadim

A Double or Triple Module Redundancy Model Exploiting Dynamic Reconfigurations ........................................... 114
Kouji Shinohara and Minoru Watanabe

Session 4: Reconfigurable Antennas

Automated Antenna Design Using Normalized Steady State Genetic Algorithm.............................................................. 125
Zhenhua Cai, Sanyou Zeng, Yang Yang, and Lishan Kang

Mapping Reconfigurable Antennas Using Graphs ........................................................................................................ 133
Joseph Costantine, Christos G. Christodoulou, and Silvio E. Barbin

A Software Defined MEMS-Reconfigurable PIXEL-Antenna for Narrowband MIMO Systems................................. 141
Alfred Grau, Jordi Romeu, Lluis Jofre, and Franco De Flaviis

Reconfigurable MEMS Antennas.................................................................................................................................. 147
Nakul Haridas, Ahmet T. Erdogan, Tughrul Arslan, Anthony J. Walton, Stewart Smith, Tom Stevenson, Camelia Dunare, Alan Gundlach, Jon Terry, Petros Argyrakis, Kevin Tierney, Alan Ross, and Tony O’Hara

Session 5: FPGAs and Reconfigurable Architectures in Space

A Novel Design Flow for the Performance Optimization of Fault Tolerant Circuits on SRAM-based FPGA’s ......................................................................... 157
Luca Sterpone and Niccolo Battezzati

A Software Defined Radio Architecture for a Regenerative Onboard Processor .............................................................. 164
Catherine Morlet, Francesca Autelitano, Gian Carlo Cardarilli, Marco Re, Enrico Petrongari, Gino Bogo, and Mario Franceschelli

Statistical Lossless Compression of Space Imagery and General Data in a Reconfigurable Architecture.................. 172
Jose Luis Nunez-Yanez, Xiaolin Chen, Nishan Canagarajah, and Raffaele Vitulli

High Performance Data Processor (HPDP) .................................................................................................................. 178
Mohsin A. Syed and Eberhard Schueler

A New System-Level Reconfigurable Lossless Image Compression System for Space Applications....................... 183
Guoxia Yu, Tanya Vladimirova, Xiaofeng Wu, and Martin N. Sweeting

Dual Core System-on-a-Chip Design to Support Inter-Satellite Communications......................................................... 191
Christopher P. Bridges and Tanya Vladimirova

Addressing Future Space Challenges using Reconfigurable Instruction Cell Based Architectures.............................. 199
Ahmed O. El-Rayis, Tughrul Arslan, and Ahmet T. Erdogan
SCARS: Scalable Self-Configurable Architecture for Reusable Space Systems............................................................204
Adarsha Sreeramareddy, Jeff Josiah, Ali Akoglu, and Adrian Stoica

Session 6: Adaptive Circuits

UbiManager: A Software Tool for Managing Ubichips ..................................................................................................213
Yann Thoma and Andres Upegui

An FPGA based Adaptive Weightless Neural Network Hardware ......................................................................................220
Pierre Lorrentz, Gareth Howells, and Klaus McDonald-Maier

Dynamic Routing on the Ubichip: Toward Synaptogenetic Neural Networks .................................................................228
Andres Upegui, Yann Thoma, Andres Perez-Uribe, and Eduardo Sanchez

Hardware Implementation of a Bio-plausible Neuron Model for Evolution and Growth of Spiking Neural Networks on FPGA ....................................................................................................................236
Hooman Shayani, Peter J. Bentley, and Andy M. Tyrrell

Adaptive Precision Neural Networks for Image Classification ..........................................................................................244
Michael J. Gilberti Jr. and Alex Doboli

Session 7: Adaptive Wireless Networks

Wireless Communication in LEO Satellite Formations ..................................................................................................255
Kawsu Sidibeh and Tanya Vladimirova

Pennies from Heaven: A Retrospective on the Use of Wireless Sensor Networks for Planetary Exploration .........................263
Robert Newman and Mohammad Hammoudeh

Lessons in Implementing Bio-inspired Algorithms on Wireless Sensor Networks ..........................................................271
Michael Breza and Julie A. McCann

Distributed Adaptability and Mobility in Space Based Wireless Pico-Satellite Sensor Networks ...........................................277
Wei Li, Tughrul Arslan, Ahmed O. El-Rayis, Nakul Haridas, Ahmet T. Erdogan, Erfu Yang

Session 8: Reconfigurable and Multi-Core Systems

A High Performance Reconfigurable Core for Motif Searching Using Profile HMM ..........................................................285
Khaled Benkrid, Panagiotis Velentzas, and Server Kasap

Simulating SiScape: A Parallel CMP Architecture ...........................................................................................................293
Dimitrios Liopis, Andreas Adamidis, and Nikolaos Theoharis

The Gannet Service Manager: A Distributed Dataflow Controller for Heterogeneous Multi-core SoCs .........................301
Wim Vanderbauwhede, Paul McKechnie, and Chidambaram Thirunavukkarasu

Dynamically Reconfigurable NoC with Bus Based Interface for Ease of Integration and Reduced Design Time .........................309
Balal Ahmad, Ali Ahmadiania, Tughrul Arslan

FPGA Implementation of Cellular Automata Spaces Using a CAM Based Cellular Architecture ........................................315
James Lloyd Weston and Peter Lee
Dynamic Reconfiguration of Mixed-Domain Embedded Systems for Applications with Variable Performance Requirements .................................................................................................................. 323
  Pengbo Sun, Michael Gilberti, Yang Zhao, Alex Doboli, Daniel Curiac, and Dan Pescaru

High-Level Modeling and Exploration of Reconfigurable MPSoCs ................................................................................................................................. 330
  Giovanni Beltrame, Luca Fossati, and Donatella Sciuto

An Overview of Low-Power Techniques for Field-Programmable Gate Arrays ............................................................................................... 338
  Julien Lamoureux and Wayne Luk

Mapping DSP Applications onto High-Performance Architectural Templates with Inlined Flexibility ...................................................................... 346
  Sotiris Xydis, George Economakos, Dimitrios Soudris, and Kiamal Pekmestzi

TLM Platform Based on SystemC for STARSoc Design Space Exploration ......................................................................................... 354
  Sami Boukhechem and El-Bay Bourennane

SystemC-based Reconfigurable IP Modelling for System-on-Chip Design .................................................................................................. 362
  Ali Ahmadinia, Balal Ahmad, Ahmet T. Erdogan, Tughrul Arslan

A Dynamically Reconfigurable Hardware Co-Processor for a Multi-Standard Wireless MAC Processor ........................................... 368
  Syed Waqar Nabi, Cade C. Wells, and Wim Vanderbauwhede

Session 9: Learning and Evolutionary Algorithms for Adaptive Hardware

DSP-Based PSO Implementation for Online Optimization of Power System Stabilizers .............................................................................. 379
  Parviz Palangpour, Pinaki Mitra, Swakshar Ray, and Ganesh K. Venayagamoorthy

FPGA Implementation of a Cellular Compact Genetic Algorithm ......................................................................................................................... 385
  Yutana Jewajinda and Prabhas Chongstitvatana

Adaptive Salt-&-Pepper Noise Removal: A Function Level Evolution based Approach .................................................................................... 391
  Jie Li and Shitan Huang

Towards Fault-Tolerant Systems based on Adaptive Cellular Genetic Algorithms .......................................................................................... 398
  Alicia Morales-Reyes, Evangelos F. Stefatos, Ahmet T. Erdogan, and Tughrul Arslan

Session 10: Special Session on ESPACENET

Investigation of Sample Sizes and Correlation in Multi-Cluster Feature Distributions for an Efficient Encryption System .............................................................................. 409
  Evangelos Papoutsis, Gareth Howells, Andrew Hopkins, and Klaus McDonald-Maier

A Framework for Self-Diagnosis and Condition Monitoring for Embedded Systems Using a SOM-Based Classifier .............................................. 417
  P. Sartain, A.B.T. Hopkins, K.D. McDonald-Mair, and W.G.J. Howells

Hardware-in-Loop Simulation of a Satellite Sensor Network for Distributed Space Applications .......................................................... 424
  Xiaofeng Wu and Tanya Vladimirova

  Erfu Yang, Ahmet T. Erdogan, Tughrul Arslan, and Nick H. Barton
Session 11: Special Session on Evolutionary and Self-Organizing and Adaptive Sensors, Actuators and Processing Hardware

Adaptation of the Perception-Action Loop Using Active Channel Sampling .................................................................443
Philippe Capdepy, Daniel Polani, and Chrystopher L. Nehaniv

A Self-Organizing Nano-Particle Simulator and Its Applications .....................................................................................451
Gibson Hu, Ying Guo, and Rongxin Li

Managing Multiple Interacting Adaptive Systems via Game Theory ..................................................................................459
David Wolpert and Nilesh Kulkarni

State-Space Modelling of Anticipatory Behaviour for Self-Adaptability with Applications to Biosensors ........467
H.J. Kadim

Session 12: Special Session on Imaging for Forensics and Security: Algorithms and Architecture

A Power Efficient Path Key Establishment Algorithm for Wireless Sensor Networks ......................................................475
Noureddine Mehalleque, Emi Garcia, Ahmed Bouridane, and Gang Qu

Contourlet Based Feature Extraction with PCA for Face Recognition ........................................................................482
Walid Riad Boukabou and Ahmed Bouridane

Automatic Recognition of Shoeprints Using Fourier-Mellin Transform ........................................................................487
Mourad Gueham, Ahmed Bouridane, Danny Crookes, and Omar Nibouche

An Eye Detector Based on Cues and Heuristics with a Good Accuracy/Complexity Trade-off ........................................492
Christos Grecos and Mingyuan Yang

Adaptive Online Profiling Hardware for ICMetrics Based Security ..................................................................................498
Andrew B.T. Hopkins, Klaus D. McDonald-Mai er, Evangelos Papoutsis, and Gareth Howells

Face Recognition Using a Cognitive Processing Model .................................................................................................505
Gorn Tepvorachai and Chris Papachristou

Fragile IP Watermarking Techniques .........................................................................................................................513
Amr T. Abdel-Hamid and Sofiene Tahar

Author Index ..................................................................................................................................................520