2013 Forum on Specification & Design Languages

(FDL 2013)

Paris, France
24-26 September 2013
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General Chairs
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Invited Speaker
Industrial Special Session Chair
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**AFM: Application of Formal Methods for Design Space Exploration and Refinement**

1. *Optimal Component Selection for Energy-Efficient Systems*  
   Matthias Sauppe, Thomas Horn, Erik Markert, Ulrich Heinkel, Hans-Werner Sahm, and Klaus-Holger Otto
2. *Assisting Refinement in System-on-Chip Design*  
   Mokrani Hocine, Ameur-Boulifa Rabéa, and Emmanuelle Encrenaz
3. *Design Space Exploration for Cyber Physical System Design using Constraint Solving*  
   Benny Höckner, Petra Hofstedt, Sascha Kaltschmidt, Peter Sauer, and Thilo Voertler

**AFM/EAMS: Verification of Heterogeneous Systems: Theory and Industrial Experiences**

1. *A New Assertion Property Language for Analog/Mixed-Signal Circuits*  
   Dhanashree Kulkarni, Andrew Fisher, and Chris Myers
2. *Integrating Circuit Analyses for Assertion-based Verification of Programmable AMS Circuits*  
   Dogan Ulus, Alper Sen, and Faik Baskaya
3. *How to Survive the Verification of the Latest Generation of Automotive System on Chip*  
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4. *A Novel Approach for Assertion Based Verification of DDR Memory Protocols*  
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