Self-Formation processes in High-Speed Integrated Circuits
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High Aspect Ratio Lateral Electrode Nano Gap Rectangular Plate Micro-Resonator Novel Process
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An Analytical Model for Spectral Peak Frequency Prediction of Substrate Noise in CMOS Substrates
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Micro- and Nanoelectronics Education in Vilnius Gediminas Technical University
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Critical Path Analysis of Two-channel Interleaved Digital MASH Delta Sigma Modulators
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Combined RF and Multilevel PWM Switch Mode Power Amplifier
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Feasibility of a cryogenic SiGe amplifier at 4 K
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Comparison of Static and Memory Predistortion in Envelope Tracking System
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Self-heating and Memory Effects in RF Power Amplifiers Explained Through Electro-Thermal Modeling
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3.5 GHz Triple Cascaded Current-Reuse Low Noise Amplifier
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Modeling and Predistortion of Envelope Tracking Power Amplifiers using a Memory Binomial Model
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A 1V SiGe Power Amplifier for 81-86 GHz E-band
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A CMOS 0.23pJ Freeze Vernier Time-to-Digital Converter
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A 7-bit 50MS/s Single-ended Asynchronous SAR ADC in 65nm CMOS
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A 31.25/125MSps Continuous–Time ΔΣ ADC with 64/59dB SNDR in 130nm CMOS
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Algorithmic Time-to-Digital Converter
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Geng Yang, Li Xie, Li-Rong Zheng, Royal Institute of Technology (SE)

Interpolation by a Prime Factor other than 2 in Low-Voltage Low-Power sigma-delta DAC
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Limited Active Harmonic Compensation in a Grid-Connected Photovoltaic Inverter
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A Simple All MOS Voltage Reference for RFID Applications
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SRT Radix-2 Dividers with (5,4) Redundant Representation of Partial Remainder
Alexandra Amaricai, Oana Boncalo, University Politehnica of Timisoara (RO)

Sleep Apnea Pre-Screening on Neonates and Children with Shoe Integrated Sensors
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Area and Power Reduction in DFT Based Channel Estimators for OFDM Systems
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A Temperature Sensor with $3\sigma$ Inaccuracy of $+0.5/-0.75$ degree C and Energy per Conversion of 0.65 $\mu$J Using a 0.18 $\mu$m CMOS Technology

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Implementation of a Highly-Parallel Soft-Output MIMO Detector with Fast Node Enumeration

Stefan Granlund, Liang Liu, Chenxin Zhang, Viktor Öwall, Lund University (SE)

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A Study of Low-Power Crystal Oscillator Design

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An FPGA-Based High-Performance Wireless Vibration Analyzer

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Highly Linear Open-loop Output Driver Design for High Speed Capacitive DACs

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Single Poly Non-Volatile Memory Cells for Miniaturized Sensors in 90nm CMOS Technology

Ali Zaher, Philipp Häfliger, Oslo University (NO)

Next Generation Digital Front-End for Multi-Standard Concurrent Reception

Isael Diaz, Chenxin Zhang, Lieven Hollevoet, Jim Svensson, Joachim Rodrigues, Liesbet Van der Perre, Lund University (SE)

A 13 bits 4.096 GHz 45 nm CMOS Digital Decimation Filter Chain Using Carry-Save Format Numbers

Yanxiang Huang, Ajay Kapoor, Robert Rutten, José Pineda de Gyvez, Technical University of Eindhoven (NL)

A Low-Power 2nd-order CT Delta Sigma Modulator with a Single Operational Amplifier

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