
London, United Kingdom
23-25 August 2017
Technical Program

Wednesday 23rd August

Image and video processing

Constantinides Session

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Chair: Wan-Chi Siu

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   Xin Lou, Wenbin Ye and Ya Jun Yu

2. Structurally orthogonal finite precision FPGA implementation of block-lifting-based quaternionic paraunitary filter banks for L2L image coding
   Nick A. Petrovsky, Eugene V. Rybenkov and Alexander A. Petrovsky

3. Cascade Non-Maximally Decimated Filter Banks Form Efficient Variable Bandwidth Filters for Wideband Digital Transceivers
   fred harris, Xiaofei Chen, Elettra Venosa and Chris Dick

4. A Merged BP Decoding Algorithm for Polar-LDPC Concatenated Codes
   Jiaai Liu, Shusen Jing, Xiaohu You and Chuan Zhang

5. Reconfigurable Fast Filter Bank Based on Node-Modulation
   Tong Ma and Ying Wei

6. An Optimal Filter for Signals with Time-Varying Cyclostationary Statistics
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