
Batumi, Georgia
5 – 8 December 2017
CONFERENCE PROGRAM

AMM-1 Analog Digital Converters
Wednesday, December 06, 2017, 11:15-13:00

1018-A 40-Ks/S 16-Bit Non-Binary SAR ADC in 0.18 μm CMOS with Noise-Tunable Comparator
Takaaki Ito, Tetsuya Iizuka, Toru Nakura, Kunihiro Asada

1029-A Low-Power Comparator-Reduced Flash ADC Using Dynamic Comparators
Hasan Molaei, Khosrow Hajsadeghi

1043-Digital Correction of Mismatches in Time-Interleaved ADCs for Digital-RF Receivers
Tomoya Takahashi, Takao Kihara, Tsutomu Yoshimura

9015-An 8 Bit 2Gs/S ADC Building Bloc Design for Nuclear Physics Instruments
Gabriel Puech

9022-Analysis of the Settling Behavior of an External Reference Voltage Source for a 16-bit and 200ms/S Pipeline Analog-To-Digital Converter
Robert Loehr, Leon Bender, Frank Ohnhaeuser, Robert Weigel

COM-1 Amplifiers in Communications
Wednesday, December 06, 2017, 11:15-13:00

1024-Matrix Single Stage Distributed Amplifier Design for Ultra Wideband Application
Temitope Odedeyi, Izzat Darwazeh

1047-A 60-90Ghz Stagger-Tuned Low-Noise Amplifier with 1.2dbm Op1db in 65nm CMOS
Ademola Mustapha

1097-A Novel Method to Design Broadband Flat Gain and Sufficiently Efficient Power Amplifiers Using Real Frequency Technique
Sedat Kilinc, Bekir Siddik Binboga Yarman

9048-Doherty CMOS Power Amplifiers for 5G Technology
Nourhan Elsayed, Hani Saleh, Baker Mohammad, Mohammed Elnaggar

1106-UFRGSplace: Routability Driven FPGA Placement Algorithm for Heterogeneous FPGAs
Julia Puget, Andre Oliveira, Jorge Seclen, Ricardo Reis

1114-A Post-Processing Methodology to Improve the Automatic Design of CMOS Gates at Layout-Level
Gustavo Smaniottto, Maicon Cardoso, Renato De Souza, Regis Zanandrea
Felipe Marques, Matheus Moreira, Leomar Rosa Jr.

9016-Construction of Coverage Data for Post-Silicon Validation Using Big Data Techniques
Eman El Mandouh, Ahmed Gamal, Ahmed Khaled, Taha Ibrahim
Elsayed Hemayed, Amr G. Wassal

9020-Smart Auto-Correction Methodology Using Assertions and Dynamic Partial Reconfiguration
Khaled Mohamed, Mohamed Abdelsalam
LNL-1 Memristive Systems
Wednesday, December 06, 2017, 11:15-13:00

1046-TiO2 Memristor Model-Based Chaotic Oscillator
Fakhreddine Zayer, Wael Dghais, Hamdi Belgacem

1092-A Novel Secure Conference Communication in IoT Devices Based on Memristors
Heba Abunahla, Dina Shehada, Chan Yeob, Baker Mohammad, Thanos Stouraitis

9023-Hybrid Memristor-CMOS Based Linear Feedback Shift Register Design
Abubaker Sasi, Amirali Amirsoleimani, Arash Ahmadi, Majid Ahmadi

9069-Coexistence of Attractors in the Parallel Inductor-Capacitor-Memristor Circuit
Zbigniew Galias

9039-A Case Study on Dynamic Control of Complex Operational Amplifier Performance Using Back Gate Biasing
Bangalore Ramesh Akshay Agashe, Elmar Herzer, Johann Hauer

AMM-2 OpAmp
Wednesday, December 06, 2017, 14:15-16:00

9004-A Fully-Differential Operational Amplifier Using a New Chopping Technique and Low-Voltage Input Devices
Timo Mai, Konstantin Schmid, Jürgen Röber, Amelie Hagelauer, Robert Weigel

9066-Method for Speeding the Micropower CMOS Operational Amplifiers with Dual-Input-Stages
Nikolay Prokopenko, Nikolay Butyrlagin, Anna Bugakova, Andrey Ignashin

1015-A Case Study on Dynamic Control of Complex Operational Amplifier Performance Using Back Gate Biasing
Bangalore Ramesh Akshay Agashe, Elmar Herzer, Johann Hauer

COM-2 Communication Circuits
Wednesday, December 06, 2017, 14:15-16:00

1057-Low-Voltage Low-Distortion Sampling Switch Design in 22 nm FD-SOI CMOS Technology
Pragoti Pran Bora, David Borggreve, Frank Vanselow, Erkan Isa, Linus Maurer

1094-Multiband and Concurrent Matching Network Design Via Brune Sections
Serkan Yıldız, Ahmet Aksen, Siddik B. Yarman

9043-The ARC-HPF with Independent Trimming of the Main Characteristics
Daria Denisenko, Yuri Ivanov, Nikolay Prokopenko

9050-A Concurrent Multiband Fully Differential CMOS LNA with a Local Active Feedback for Cellular Applications 3G-4G
Hakan Çetinkaya, Tufan Karalar, Siddik Yarman

9071-Design of 0-15GHZ Band 180 Degree Digital Phase Shifting Cell Topology
Celal Avci, Ece Olcay Gunes, Binboga Siddik Yarman
EDA-2 Electronic Design
Wednesday, December 06, 2017, 14:15-16:00

1023-Spice Simulation of Ferrite Core Losses and Hot Spot Temperature Estimation .......................................................... 107
Sam Ben-yaakov

1065-Application Specific Design for Digital Beam-former (DBF) .............................................. 111
Ahood Al Jneibi, Menatalla Hassan, Hani Saleh, Lilas Alrahis, Tasneem Assaf

1077-The Assessment of the Antenna Circuit of a RF Wheel Unit ........................................... 116
Emil Lazarescu, Florin Alexa, Doru Vatau, Flaviu Mihai Frigura-iliasa

1095-Design Considerations for Near to the Ground Communication System and Associated Sub-GHz Low Profile Antenna ......................................................... 120
Vadi Su Yilmaz, Gulsima Bilgin, Elif Aydin, Ali Kara

9070-Automatic Sensitivity Analysis Tool for Analog Active Filter ...................................... 124
Amin Sallem, Pedro Pereira, Mourad Fakhfakh

LNL-2 Neural Networks
Wednesday, December 06, 2017, 14:15-16:00

1034-Learning and Real-Time Classification of Hand-Written Digits with Spiking Neural Networks ......................................................... 128
Shruti R. Kulkarni, John M. Alexiades, Bipin Rajendran

1038-Low Power Neuromorphic Hardware Based Multi-Modal Authentication System ............................................................. 132
Manan Suri, Shridu Verma, Narayani Bhatia, Salam Thoi Thoi Singh

1086-Evaluating Reconfigurable Substrates for Intelligent Hardware Systems ..................... 136
Omar Eldash, Kasem Khalil, Magdy Bayoumi

1115-Hand Gesture Classification Using Inertial Based Sensors Via a Neural Network .................................................................................. 140
Erhan Akan, Hakan Tora, Baran Uslu

9038-Subset Selection for Tuning of Hyper-Parameters in Artificial Neural Networks ............................................................. 144
K. K. Emre Aki, Tugba Erkoç, M. Taner Eskil
AMM-3 Analog Filters
Wednesday, December 06, 2017, 16:15-18:00

1037-A New Current Mode Implementation of the Reconfigurable Analog Baseband Low Pass Filter with Cell-Based Variable Transconductance Amplifier ........ 148 Ersin Alaybeyoglu, Hakan Kuntman

1075-0.5-V Bulk-Driven Quasi-Floating Gate Transconductance Amplifier ............... 152 Montree Kumngern, Usa Torteanchai, Fabian Khateb

1121-Design of Integer/Fractional-Order Filter with Electronically Reconfigurable Transfer Response Using Commercially Available Elements ............. 156 Roman Sotner, Norbert Herencsar, Jan Jerabek, Jiri Petrzela, Tomas Dostal

9037-Novel MS2-Type FDNR Simulation Configuration with Electronic Control and Grounded Capacitances ................................................................................ 160 Dinesh Prasad, Mayank Srivastava

9047-Integrator with P-Channel Depletion MOS Switch .......................................... 165 Huseyin Ozgur Kazanci

COM-3 FPGA Applications
Wednesday, December 06, 2017, 16:15-18:00

1025-Evaluation of Noc on Multi-FPGA Interconnection Using GTX Transceiver ........ N/A Atef Dorai, Olivier Sentieys, Helene Dubois

1030-FPGA-Based Implementation of a Frequency Spreading FBMC-OQAM Baseband Modulator ............................................................................................................. 174 Miguel Carvalho, Mario Lopes Ferreira, Joao Canas Ferreira

1082-Efficient FPGA Implementation of Probabilistic Gallager B LDPC Decoder ........ 178 Fakhreddine Ghaffari, Burak Unal, Ali Akoglu, Khoa Le, David Declercq, Bane Vasic

9042-HLTB Design for High-Speed Multi-FPGA Pipelines ........................................ 182 Josef Magri, Owen Casha, Keith Bugeja, Edward Gatt, Ivan Grech

ETD-1 Emerging Systems
Wednesday, December 06, 2017, 16:15-18:00

1013-A Cell Clustering Technique to Reduce Transistor Count .................................... 186 Calebe Conceicao, Gisell Moura, Filipe Pisoni, Ricardo Reis

1039-Capacitive Sensor Technology for Soil Moisture Monitoring Networks .............. 190 Schubert Martin J. W., Seign Stefan, Dai Qibin, Hinterseer Sebastian, Pielmeier Florian, Yu Changqing, Zenger Stefan

1058-Supplementary Mos-Only Butterworth LP BP Filter Circuits ................................ 194 Oguzhan Cicekoglu, Norbert Herencsar, Bilgin Metin

9032-Challenges for Fully-Integrated Resonant Switched Capacitor Converters in CMOS Technologies ................................................................. 198 Yasser Moursy, Anthony Quelen, Gael Pillonnet

1089-Simplified Symps Codec with Positive DC Offset .................................................. 202 Binboga Siddik Yarman, Osman Korkmaz, Mehmet Temizyurek, Fuat Ince

Basak Hassoy, Haluk Canberi, Erhan Ceran, Emre Gultekin
LNL-3 Non-Linear Systems
Wednesday, December 06, 2017, 16:15-18:00

1055-Study of Two-Memristor Circuit Model with Explicit Composition Method .......... 206
Denis Butusov, Valerii Ostrovskii, Alexander Zubarev
1093-Stochastic Bifurcation in Generalized Chua's Circuit Through Alpha-Stable
Levy Noise ......................................................................................................................... 210
Serpil Yilmaz, Ferit Acar Savaci
1104-Stochastic Deep Learning in Memristive Networks.............................................. 214
Anakha V Babu, Bipin Rajendran
9017-A CMOS Programmable Phase Shifter for Compensating Synchronous
Detection Bioimpedance Systems..................................................................................... 218
Panagiotis Kassanos, Guang-zhong Yang
9062-Nonlinear Modeling and Analysis of Buck Converter Using Volterra Series .......... 222
Dr. Hitesh Shrimali, Vijender Kumar Sharma, Dr. Jai Narayan Tripathi, Rakesh Malik

Invited Special Session

Thursday, December 07, 2017,

1070-Sampling Based Random Number Generator for Stochastic Computing ............. 227
M.Burak Karadeniz, Mustafa Altun
AMM-4 Analog Circuits
Thursday, December 07, 2017, 11:15-13:00

1019-An Ultra-Wide-Range Fine-Resolution Two-Step Time-To-Digital Converter with Built-In foreground Coarse Gain Calibration .................................................. 231
Ryuichi Enomoto, Tetsuya Iizuka, Toru Nakura, Kunihiro Asada

9028-A Low Phase Noise CMOS Oscillator with Simple Tail Current-Shaping in Wireless SOC ....................................................................................................................... 235
Amir Mahdavi

9029-A Robust and Low-Power Synchronization Technique of Coarse-And-Fine Conversion Parts in Ring-Oscillator-Based Time-To-Digital Converters ........................... 239
Takayuki Okazawa, Ippei Akita

9030-On the Jitter-To-Fast-Clock-Period Ratio in Oscillator-Based True Random Number Generators ................................................................................................. 243
Eduardo Bejar, Julio Saldaña, Erick Raygada, Carlos Silva

COM-4 Communications
Thursday, December 07, 2017, 11:15-13:00

1067-Performance Analysis of the Energy Efficient Clustering Models in Wireless Sensor Networks .................................................................................................................. 247
Sercan Vançin, Ebubekir Erdem

1072-XGT4: An Industrial Grade, Open Source Tester for Multi-Gigabit Networks Juracy Leonardo, Lazzarotto Felipe, Pigatto Daniel, Calazans Ney, Moraes Fernando

9013-A Novel Time Slot Assignment Method in Mobile Mesh Networks ........................................ 256
Tugrul Akyuz, Omer Aydin

9040-Spur Reduction By Self-Injection Loop in a Fractional-N PLL ........................................ 260
Mayu Kobayashi, Yuya Masui, Takao Kihara, Tsutomu Yoshimura

9063-Efficient Polynomial Regression Algorithm for Lte Turbo Decoding ................................ 264
Mostafa Foda, Mohamed Abd El Ghany, Klaus Hoffman

ETD-2 Technics in Emerging Technologies
Thursday, December 07, 2017, 11:15-13:00

1016-Temperature Dependence and Ztc Bias Point Evaluation of Sub 20nm Bulk Multigate Devices ................................................................................................................ 270
Ygor Aguiar, Alexandra Zimpeck, Cristina Meinhardt, Ricardo Reis

1042-Design and Implication of a Rule Based Weight Sparsity Module in HTM Spatial Pooler .......................................................................................................................... 274
Timur Ibrayev, Olga Krestinskaya, Alex James

1080-The Art of Extracting Circuit Transfer Functions with Sutra Analyses ................................ 278
Sotoudeh Hamedi-Hagh

1101-Set Response of FinFET-Based Majority Voter Circuits Under Work-Function Fluctuation .................................................................................................................. 282
Ygor Aguiar, Fernanda Kastensmidt, Cristina Meinhardt, Ricardo Reis

9027-Analytical Modeling of Transconductance in Organic Thin-Film Transistors ........ 286
Reza Meshkin, Seyed Ali Sedigh Ziabari
VLSI-1 Systems Perspective
Thursday, December 07, 2017, 11:15-13:00

1031-Impact of Schmitt Trigger Inverters on Process Variability
Robustness of 1-Bit Full Adders ................................................................. 290
Samuel Presa Toledo, Alexandra L. Zimpeck, Cristina Meinhardt

1053-Comparing 32nm Full Adder TMR and DTMR Architectures ........... 294
Giane Ulloa, Vinicius Lucena, Cristina Meinhardt

1119-Exploring The Use of Parallel Prefix Adder Topologies Into
Approximate Adder Circuits ................................................................. 298
Morgana Macedo Azevedo Da Rosa, Leonardo Soares, Bianca Silveira
Claudio M. Diniz, Eduardo A. C. Da Costa

1120-Exploring the Combination of Number of Bits and Number of Iterations
for a Power-Efficient Fixed-Point Cordic Implementation .................... 302
Andre Sapper, Leonardo Soares, Eduardo Costa, Sergio Bampi

AMM-5 Analog Circuits
Thursday, December 07, 2017, 14:15-16:00

1052-Assessment of Seven Reconstruction Methods for Contemporary
Compressive Sensing .................................................................................. 306
Hamza Al Maharmeh, Hani Saleh, Baker Mohammad, Mohammad Ismail
Thanos Stouraitis

1051-A Low-Power, High-Resolution, 1 Ghz Differential Comparator with
Low-Offset and Low-Kickback ................................................................. 310
Mehdi Nasrollahpour, Muhammad Aldacher, Sotoudeh Hamedi-Hagh

1105-Hardening C-Elements Against Metastability .................................. 314
Leandro Heck, Matheus Moreira, Ney Calazans

9008-A Triangular Active Charge Injection Scheme Using a Resistive
Current for Resonant Power Supply Noise Suppression .......................... 318
Masahiro Kano, Toru Nakura, Tetsuya Iizuka, Kunihiro Asada

9012-Compact First Order Temperature-Compensated CMOS Current Reference ...... 322
Dmitry Osipov, Steffen Paul

9014-Digital Fcs-Mp Control of an AC-DC Power Converter to Improve
Dynamic Response .................................................................................. 326
Abdolsamad Hamidi, Shahram Karimi, Arash Ahmadi, Majid Ahmadi
VLSI-2 Realization
Thursday, December 07, 2017, 14:15-16:00

1054-Self-Healing Router Architecture for High Performance Network-On-Chips ........ 330
Kasem Khalil, Omar Eldash, Magdy Bayoumi

1064-A 65nm ASIC Design for Measuring Mental Stress From The Heart Rate Variations ................................................................................................................................. 334
Huda Goian, Aamna Alali, Temesghen Habte, Hani Saleh, Tasneem Assaf

1081-Car: on the Highway Towards De-Synchronization ................................................... 339
Francois Bertrand, Abdelkarim Cherkaoui, Jean Simatic, Anthony Maure, Laurent Fesquet

9057-An Aging-Aware Model for the Leakage Power of Nano-Scaled Digital Integrated Circuits in Iot Era................................................................. 343
Amirhossein Moshrefi, Hossein Aghababa

ADP-2 Imaging
Thursday, December 07, 2017, 16:15-18:00

1033-Optimization and Hardware Implementation of Image Watermarking for Low Cost Applications ............................................................................................................ 347
Konstantinos Pexaras, Christos Tsiorakis, Irene Karybali, Emmanouil Kalligeros

1091-The Effect of Secret Image Transformation on the Steganography Process ............ 351
Mohamed Beker, Hakan Tora, Erhan Gokcay

1112-Reversible Watermarking in Medical Imaging with Zero Distortion in ROI ............. 356
Ales Rocek, Michal Javorník, Karel Slavicek, Otto Dostal

9005-An Intelligent Readout Integrated Circuit (IROIC) with On-Chip Local Gradient Operations ............................................................................................................... 360
Javier Soto, Wladimir Valenzuela, Silvana Díaz, Antonio Saavedra
Miguel Figueroa, Javad Ghasemi, Payman Zarkesh-ha

9044-Modular Evaluation System for Low-Power Applications ........................................ 364
Andrea Schwandt, Marco Winzker

AMM-6 Analog Mixed Mode Circuits
Thursday, December 07, 2017, 16:15-18:00

1100-A 28um2,0.11hz,4.5pw Gate Leakage Timer Using Differential Leakage Technique in 55nm Ddc CMOS for Small-Footprint,Low-Frequency and Low-Power Timing Generation ...................................................................................... 368
Yuya Nishio, Atsuki Kobayashi, Kiichi Niitsu

1012-The Implementation of a Successive Cancellation Polar Decoder on Xilinx System Generator ........................................................................................................ 372
A.Cagri Arli, Ayse Colak, Orhan Gazi

1050-Level-Shifted Neural Encoded Analog-To-Digital Converter.................................... 377
Aigerim Tankimanova, Akshay Maan, Alex James

1103-Go Functional Model for a Risc-V Asynchronous Organization-ARV .......................... 381
Marcos Sartori, Ney Calazans

9035-A 5.9 Ghz RF Rectifier for Wireless Power Transmission Applications .................. 385
Mariem Kanoun, David Cordeau, Jean Marie Paillot, Hassene Mnif, Mourad Loulou
### EDA-1 Digital Applications
**Wednesday, December 06, 2017, 11:15-13:00**

<table>
<thead>
<tr>
<th>Paper Number</th>
<th>Title</th>
<th>Authors</th>
</tr>
</thead>
<tbody>
<tr>
<td>1027</td>
<td>Adaptive Face Space Models with Dynamic Neural Priors and Sparse Coding</td>
<td>Samira Reihanian, Amin Zollanvari, Alex Pappachen James</td>
</tr>
<tr>
<td>1032</td>
<td>Investigating Parallel Tmr Approaches and Thread Disposable in Linux</td>
<td>Gennaro Rodrigues, Felipe Rosa, Fernanda Kastensmidt, Ricardo Reis Luciano Ost</td>
</tr>
<tr>
<td>9009</td>
<td>Medical Images Protection and Authentication Using Hybrid DWT-DCT and SHA256-MD5 Hash Functions</td>
<td>Alavi Kunhu, Hussain Al-ahmad, Fatma Taher</td>
</tr>
<tr>
<td>9051</td>
<td>Analysis of Neural Activity From Eeg Data Using Emd Frequency Bands</td>
<td>Maximiliano Bueno-lopez, Eduardo Giraldo, Marta Molinas</td>
</tr>
<tr>
<td>1113</td>
<td>A Scalable Time-Domain Biosensor Array Using a Capacitor-Less CMATC and Logarithmic Cyclic Time-Attenuation-Based TDC with Discharge Acceleration for High-Spatial-Resolution Bio-Imaging</td>
<td>Kei Ikeda, Atsuki Kobayashi, Kiichi Niitsu</td>
</tr>
</tbody>
</table>

### BEC Bio-Devices
**Friday, December 08, 2017, 11:15-13:00**

<table>
<thead>
<tr>
<th>Paper Number</th>
<th>Title</th>
<th>Authors</th>
</tr>
</thead>
<tbody>
<tr>
<td>1014</td>
<td>A 115v Bi-PhASIC Pulse Electrical Muscle Stimulator By Using Inductor-Sharing Dual-Output Boost Converter with Supply-Stepping Switch Driver</td>
<td>Atit Tamtrakarn</td>
</tr>
<tr>
<td>1063</td>
<td>Automatic Detection of Coronary Artery Disease (CAD) in an ECG Signal</td>
<td>Ayesha Alhosani, Sara Alshizawi, Shayma Alali, Hani Saleh, Tasneem Assaf Thanos Stouraitis</td>
</tr>
<tr>
<td>1066</td>
<td>A New Bio-Implantable Transmitter Topology for Minimizing the Antenna Effects</td>
<td>Mojtaba Daliri</td>
</tr>
<tr>
<td>9036</td>
<td>Research of Limiting Characteristics of Peak Detector for Analog Interface in Impedance Spectroscopy Systems</td>
<td>Leontiy Samoilov, Evgeniy Zhebrun, Petr Budyakov</td>
</tr>
</tbody>
</table>
LPE-1 Low Power & Energy Harvesting
Friday, December 08, 2017, 11:15-13:00

1020-A Low-Voltage Hysteresis Comparator for Low Power Applications ...................... 427
Takumi Saito, Satoshi Komatsu

1076-RF Energy Harvesting System and Circuits for Charging of Wireless Devices Using Spectrum Sensing ........................................................................................................... 431
Naser Ahmadi Moghaddam, Alireza Maleki, Navid Shahbazi Panah

9025-A Framework for System Level Low Power Design Space Exploration ................... 437
Ameni Ben Mrad, Michel Auguin, François Verdier, Amal Ben Ameur

9045-A CMOS MPPT Power Conditioning Circuit for Energy Harvesters ...................... 442
Francarl Galea, Owen Casha, Ivan Grech, Edward Gatt, Joseph Micallef

9061-Characterization of RF Energy Harvesting at 2.4 GHz .............................................. 446
Hadeel Aboueidah, Nasma Abbas, Nadeen El-nachar, Aya Alyousef
Mohammad Alhawari, Baker Mohammad, Hani Saleh, Tasneem Assaf
Mohammed Ismail

9010-The Application of the Multivalued Logical Elements “Minimum” and “Maximum” for Processing Current Signals of Sensors.......................................................... 450
Nikolay Chernov, Nikolay Prokopenko, Vladislav Yugai, Nikolay Butyrlagin

Special Session - In Memoriam
Thursday, 7 December 2017 16:15 - 18:00

9100 - Design of Broadband Microwave Power Amplifiers via Fettweis Representation of Brune Functions ........................................................................................................... 546
Siddik Yarman

9101 – In memory of Tamás Roska (1940-2014) ................................................................. 550
Péter Szolgay

9102 - “Professor of Professors” Yılmaz TOKAD: “I have always loved academic life” ................................................................................................................................. 552
Erol Sezer

9103 - Radoslav Horvat and Mirko Milić, Founders of Circuit Theory in Former Yugoslavia ......................................................................................................................... 557
Ljiljana Milić, Ljiljana Trajković
PS1-Poster Session
Wednesday, December 06, 2017, 14:15-16:00

1028-A Memristor Low-IF IQ-Mixer in Metalmumps: Modelling and Simulation
Jeremy Scerri, Ivan Grech, Edward Gatt, Owen Casha

1041-Arbitrary Generation of Phase Shift By Pseudo-Differential Bilinear Sections and Their Application
Roman Sotner, Jan Jerabek, Jiri Petrzela, Norbert Herencsar, Roman Prokop, Tomas Dostal

1056-All-Digital 1550 nm Optical Aqueous Glucose Solution Measurement System
Volkan Turgul, Izzet Kale

1062-Application-Specific Processor for Local-Binary-Patterns Generation
Bilal Taha, Lilas Alrahis, Tasneem Assaf, Naoufel Werghi, Hani Saleh, Thanos Stouraitis

1084-System-on-a-Chip (SOC)-Based Hardware Acceleration for Extreme Learning Machine
Amin Safaei, Q. M. Jonathan Wu, Yimin Yang, Thangarajah Akilan

1087-A VCO-Free Low-Power Fully Digital BPSK Demodulator for Implantable Biomedical Microsystems
Mahdi Hosseinnejad, Abbas Erfanian

1102-Framework-Based Arithmetic Core Generation to Explore ASIC-Based Parallel Binary Multipliers
Leandro Rocha, Guilherme Paim, Rafael Ferreira, Eduardo Costa, Sergio Bampi

1107-Improved Goldschmidt Algorithm for Fast and Energy-Efficient Fixed-Point Divider
Pedro Marques, Guilherme Paim, Eduardo Costa, Sergio Almeida

1116-Using Efficient Adder Compressors with a Split-Radix Butterfly Hardware Architecture for Low-Power IoT Smart Sensors
Gustavo Santana, Guilherme Paim, Leandro Rocha, Mateus Fonseca

1117-Using Adder and Subtractor Compressors to Sum of Absolute Transformed Differences Architecture for Low-Power Video Encoding
Brunno Abreu, Mateus Grellert, Guilherme Paim, Rafael Ferreira

9041-The Design of Memristor Based High Pass Filter Circuits
Muhammet Emin Sahin, Hasan Guler
PS2- Poster Session
Wednesday, December 06, 2017, 16:15-18:00

1021-An Ultra-Low-Power Supercapacitor Voltage Monitoring System for Low-Voltage Energy Harvesting
Takanori Sato, Tetsuya Hirose, Hiroki Asano, Nobutaka Kuroki, Masahiro Numa

1022-A 0.1-0.6 V Input Range Voltage Boost Converter with Low-Leakage Driver for Low-Voltage Energy Harvesting
Yuto Tsuji, Tetsuya Hirose, Toshihiro Ozaki, Hiroki Asano, Nobutaka Kuroki, Masahiro Numa

1040-Aging Aware Safe Operating Area Investigation of Switching Converter Output Stages Through 2D Plots
Engin Afacan, Kemal Ozanoglu, Merve Toka

1044-Linear Feedback in Nonlinear Circuits
Zhenlong Xiao

1083-An Energy-Efficient FPGA-Based Matrix Multiplier
Yiyu Tan, Toshiyuki Imamura

1096-Instruction-Level Programming Approach for Very Long Instruction World Digital Signal Processors
Tomas Fryza, Roman Mego

1099-Exploiting Absolute Arithmetic for Power-Efficient Sum of Absolute Differences
Bruno Abreu, Guilherme Paim, Mateus Grellert, Bianca Silveira, Claudia Diniz, Eduardo Costa, Sergio Bampi

1118-Physical Implementation of an ASIC-Oriented SRAM-Based Viterbi Decoder
Leandro Rocha, Guilherme Paim, Gustavo Santana, Rafael Ferreira, Eduardo Costa, Sergio Bampi

9026-Co-Verification Design Flow for HDL Languages
Laurent Beaulieu, Olivier Weppe, Benoit Le Lede, Florien Lebeau

9088-Yield Analysis of Nano-Crossbar Arrays for Uniform and Clustered Defect Distributions
Onur Tunal, Mustafa Altiparmak

9090-Design of a High Gain Telescopic-Cascode Operational Amplifier Based on the ZTC Operation Condition
Lida Kouhalvandi, Sercan Aygun, Ece Olcay Gunes, Murvet Kirci

9091-An Improved 2 Stage Opamp with Rail-To-Rail Gain-Boosted Folded Cascode Input Stage and Monticelli Rail-To-Rail Class Ab Output Stage
Lida Kouhalvandi, Sercan Aygun, Ece Olcay Gunes, Murvet Kirci